IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mari Named

Inventor

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Title

POWER SEMICONDUCTOR SWITCHING

DEVICES WITH LOW POWER LOSS AND METHOD FOR FABRICATING THE

SAME

Docket No.

B784.312-1

Group Art Unit: 2826

Examiner: M. L. Tran

EXPRESS MAIL COVER SHEET

SENT VIA EXPRESS MAIL

Assistant Commissioner for Patents Washington, D.C. 20231

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Sir:

The following papers are being transmitted via EXPRESS MAIL to the U.S. Patent

and Trademark Office on the date shown below:

1. Substitute Response

2. Itemized Return Receipt Postcard

Respectfully submitted,

KINNEY & LANGE, P.A.

Date: January 21,2003

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